

STN2907AEF

PNP Silicon Transistor

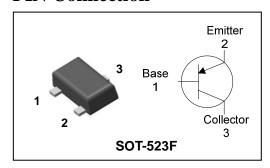
Descriptions

- General purpose application
- Switching application

Features

- Large collector current
- Low collector saturation voltage

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
STN2907AEF	<u>GT</u> □ ① ②	SOT-523F

①Device Code ② Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Collector-Base voltage	V_{CBO}	-55	V
Collector-Emitter voltage	$V_{\sf CEO}$	-50	V
Emitter-Base voltage	V_{EBO}	-5	V
Collector current	I _C	-600	mA
Collector Power dissipation	$P_{C}^{^{\star}}$	150	mW
Junction temperature	T _j	150	°C
Storage temperature range	T_{stg}	-55~150	°C

^{* :} Package mounted on 99.5% Alumina 10×8×0.1mm.

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Emitter breakdown voltage	BV _{CEO}	I _C =-10mA, I _B =0	-50	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} =-55V, I _E =0	-	ı	-10	nA
Emitter cut-off current	I _{EBO}	$V_{EB} = -5V, I_{E} = 0$	-	-	-10	nA
DC current gain	h _{FE}	V _{CE} =-10V, I _C =-10mA	75	-	-	-
Collector-Emitter saturation voltage	V _{CE(sat)}	I _C =-150mA, I _B =-15mA	-	-	-0.4	V
Transition frequency	f _T	V_{CE} =-5V, I_{C} =-20mA	250	-	-	MHz
Collector output capacitance	C _{ob}	V _{CB} =-10V, I _E =0, f=1MHz	-	6.0	-	pF

Electrical Characteristic Curves

Fig. 1 P_C - T_a

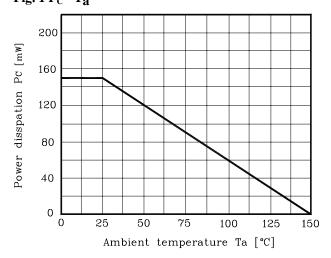


Fig. 2 I_C - V_{BE}

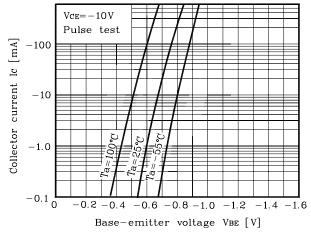


Fig. 3 I $_{\rm C}$ - $V_{\rm CE}$

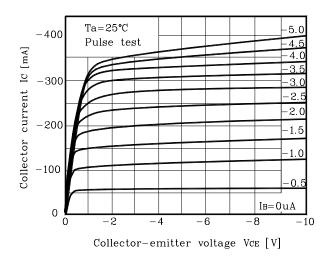


Fig. 4 $V_{CE(sat)}$ - I_C

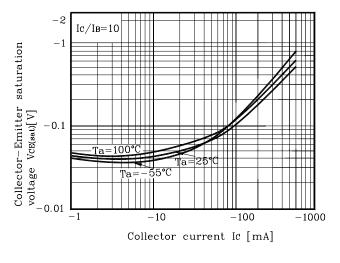


Fig. 5 h_{FE} - I_C

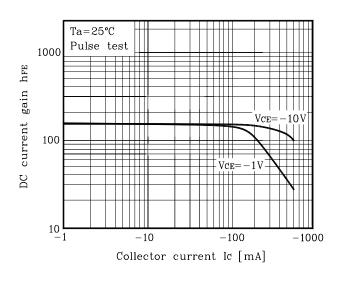
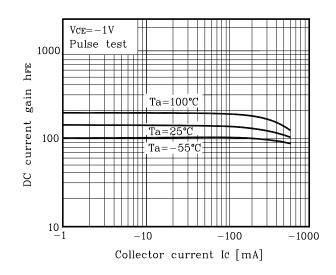
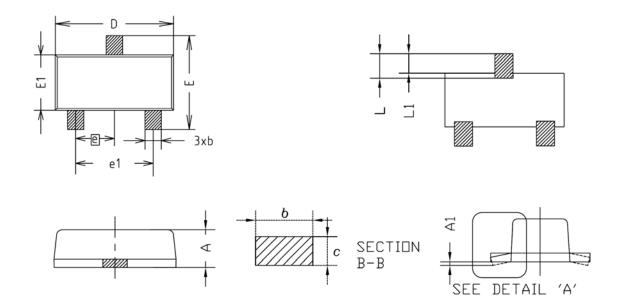


Fig. 6 h_{FE} - I_{C}



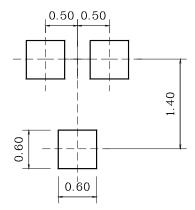
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Outline Dimension



SYMBOL	MILLIMETERS			NOTE
STILL	MINIMUM	NOMINAL	MAXIMUM	NUIL
Α	0.63	0.68	0.73	
A1	0.00	_	0.10	
A2	_	_	_	
b	0.25	0.30	0.35	
_	0.04	0.11	0.20	
D	1.50	1.60	1.70	
Ε	1.50	1.60	1.70	
E1	0.78	0.88	0.98	
е	0.50BSC			
e1	0.90	-	1.10	
L	0.34	0.44	0.54	
L1	0.28	0.34	0.43	

***Recommend PCB solder land [Unit: mm]**



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